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The time in the **status manager**'s window is the time it locked up. ... Bit 6 CRC

Data Field. This one deserves a direct quote: "This bit is set when a CRC ...

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Translator's clients, which are The swath manager and The **status manager**. ...

field indicates The number of bytes in The **data field**. The **data field** does not ...

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in addition to The internal display **data register**, The LCD module includes an

... to connect either The display **data register** or The control/status register ...

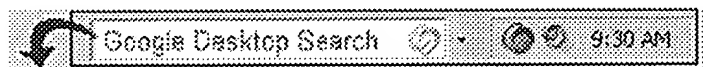
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in addition to the internal display **data register**, the LCD module includes an

... to connect either the display **data register** or the control/status register ...

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Relevance scale ☐ ☐ ☐ ☐ ☐**1 [Fast detection of communication patterns in distributed executions](#)**

Thomas Kunz, Michiel F. H. Seuren

November 1997 **Proceedings of the 1997 conference of the Centre for Advanced Studies on Collaborative research**Full text available: [pdf\(4.21 MB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Understanding distributed applications is a tedious and difficult task. Visualizations based on process-time diagrams are often used to obtain a better understanding of the execution of the application. The visualization tool we use is Poet, an event tracer developed at the University of Waterloo. However, these diagrams are often very complex and do not provide the user with the desired overview of the application. In our experience, such tools display repeated occurrences of non-trivial commun ...

2 [Data-Driven and Demand-Driven Computer Architecture](#)

Philip C. Treleaven, David R. Brownbridge, Richard P. Hopkins

January 1982 **ACM Computing Surveys (CSUR)**, Volume 14 Issue 1Full text available: [pdf\(4.14 MB\)](#) Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)**3 [Distributed operating systems](#)**

Andrew S. Tanenbaum, Robbert Van Renesse

December 1985 **ACM Computing Surveys (CSUR)**, Volume 17 Issue 4Full text available: [pdf\(5.49 MB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#), [review](#)

Distributed operating systems have many aspects in common with centralized ones, but they also differ in certain ways. This paper is intended as an introduction to distributed operating systems, and especially to current university research about them. After a discussion of what constitutes a distributed operating system and how it is distinguished from a computer network, various key design issues are discussed. Then several examples of current research projects are examined in some detail ...

4 [WSQ/DSQ: a practical approach for combined querying of databases and the Web](#)

Roy Goldman, Jennifer Widom

May 2000 **ACM SIGMOD Record , Proceedings of the 2000 ACM SIGMOD international conference on Management of data**, Volume 29 Issue 2Full text available: [pdf\(223.65 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)


We present WSQ/DSQ (pronounced "wisk-disk"), a new approach for combining the query

facilities of traditional databases with existing search engines on the Web. WSQ, for *Web-Supported (Database) Queries*, leverages results from Web searches to enhance SQL queries over a relational database. DSQ, for *Database-Supported (Web) Queries*, uses information stored in the database to enhance and explain Web searches. This paper focuses primarily on WSQ, describing a simple, lo ...

5 801 storage: architecture and programming

Albert Chang, Mark F. Mergen

February 1988 **ACM Transactions on Computer Systems (TOCS)**, Volume 6 Issue 1

Full text available:  [pdf\(1.87 MB\)](#)


Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#), [review](#)

Based on novel architecture, the 801 minicomputer project has developed a low-level storage manager that can significantly simplify storage programming in subsystems and applications. The storage manager embodies three ideas: (1) large virtual storage, to contain all temporary data and permanent files for the active programs; (2) the innovation of database storage, which has implicit properties of access serializability and atomic update, similar to those o ...

6 Balancing performance and flexibility with hardware support for network architectures

Ilija Hadžić, Jonathan M. Smith

November 2003 **ACM Transactions on Computer Systems (TOCS)**, Volume 21 Issue 4

Full text available:  [pdf\(719.03 KB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)


The goals of performance and flexibility are often at odds in the design of network systems. The tension is common enough to justify an architectural solution, rather than a set of context-specific solutions. The Programmable Protocol Processing Pipeline (P4) design uses programmable hardware to selectively accelerate protocol processing functions. A set of field-programmable gate arrays (FPGAs) and an associated library of network processing modules implemented in hardware are augmented with so ...

Keywords: FPGA, P4, computer networking, flexibility, hardware, performance, programmable logic devices, programmable networks, protocol processing

7 Special issue: AI in engineering

D. Sriram, R. Joobani

April 1985 **ACM SIGART Bulletin**, Issue 92

Full text available:  [pdf\(8.79 MB\)](#)


Additional Information: [full citation](#), [abstract](#)

The papers in this special issue were compiled from responses to the announcement in the July 1984 issue of the SIGART newsletter and notices posted over the ARPAnet. The interest being shown in this area is reflected in the sixty papers received from over six countries. About half the papers were received over the computer network.

8 ARPS: a new real-time computer

Kenneth J. Thurber

October 1976 **ACM SIGARCH Computer Architecture News**, Volume 5 Issue 4


Full text available:  [pdf\(1.14 MB\)](#)

Additional Information: [full citation](#), [references](#), [citations](#)

9 Application of intelligent agent technology for managerial data analysis and mining

Ranjit Bose, Vijayan Sugumaran

January 1999 **ACM SIGMIS Database**, Volume 30 Issue 1

Full text available:  [pdf\(1.96 MB\)](#)

Additional Information: [full citation](#), [abstract](#), [index terms](#)

Data analysis and mining technologies help bring business intelligence into organizational decision support systems (DSS). While a myriad of data analysis and mining technologies are commercially available today, organizations are seeing a growing gap between powerful

storage (data warehouse) systems and the business users' ability to analyze and act effectively on the information they contain. We contend that to narrow this gap effectively, a data analysis and mining environment is needed that ...

Keywords: agent-based design, data mining, data warehouse, decision support systems, intelligent agents, multidimensional analysis, prototype implementation, statistical analysis, visualization

10 The KScalar simulator

J. C. Moure, Dolores I. Rexachs, Emilio Luque

March 2002 **Journal on Educational Resources in Computing (JERIC)**, Volume 2 Issue 1

Full text available:  pdf(493.35 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)


Modern processors increase their performance with complex microarchitectural mechanisms, which makes them more and more difficult to understand and evaluate. KScalar is a graphical simulation tool that facilitates the study of such processors. It allows students to analyze the performance behavior of a wide range of processor microarchitectures: from a very simple in-order, scalar pipeline, to a detailed out-of-order, superscalar pipeline with non-blocking caches, speculative execution, and comp ...

Keywords: Education, pipelined processor simulator

11 Architecture of the IBM system/370

Richard P. Case, Andris Padegs

January 1978 **Communications of the ACM**, Volume 21 Issue 1

Full text available:  pdf(2.78 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)


This paper discusses the design considerations for the architectural extensions that distinguish System/370 from System/360. It comments on some experiences with the original objectives for System/360 and on the efforts to achieve them, and it describes the reasons and objectives for extending the architecture. It covers virtual storage, program control, data-manipulation instructions, timing facilities, multiprocessing, debugging and monitoring, error handling, and input/output operations. ...

Keywords: architecture, computer systems, error handling, instruction sets, virtual storage

12 Client-server computing

Alok Sinha

July 1992 **Communications of the ACM**, Volume 35 Issue 7

Full text available:  pdf(7.53 MB) Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#), [review](#)

Keywords: client-server computing

13 Data base directions: the next steps

John L. Berg

November 1976 **ACM SIGMOD Record , ACM SIGMIS Database**, Volume 8 , 8 Issue 4 , 2

Full text available:  pdf(9.95 MB) Additional Information: [full citation](#), [abstract](#)

What information about data base technology does a manager need to make prudent decisions about using this new technology? To provide this information the National Bureau of Standards and the Association for Computing Machinery established a workshop of


approximately 80 experts in five major subject areas. The five subject areas were auditing, evolving technology, government regulations, standards, and user experience. Each area prepared a report contained in these proceedings. The proceedings p ...

Keywords: DBMS, auditing, cost/benefit analysis, data base, data base management, government regulation, management objectives, privacy, security, standards, technology assessment, user experience

14 An approach to support automatic generation of user interfaces

Prasun Dewan, Marvin Solomon

October 1990 **ACM Transactions on Programming Languages and Systems (TOPLAS)**, Volume 12 Issue 4

Full text available:  pdf(3.55 MB)


Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

In traditional interactive programming environments, each application individually manages its interaction with the human user. The result is duplication of effort in implementing user interface code and nonuniform—hence confusing—input conventions. This paper presents an approach to support automatic generation of user interfaces in environments based on algebraic languages. The approach supports the editing model of interaction, which allows a user to view all appli ...

15 An on-line simulator and database system for management of a commercial fish farm

A. Neil Arnason, Carl J. Schwarz, David H. Scuse

January 1981 **Proceedings of the 13th conference on Winter simulation - Volume 1**

Full text available:  pdf(1.33 MB)


Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

We have developed an on-line system to aid in the management of enclosed, multi-tank fish farms with water filtration, heating and recirculation, where fish are grown from fingerling through to market size (about 250 gr.). The manager has a high degree of control over tank loadings and fish rearing conditions, but he must maintain high production rates within biological constraints on water quality and fish density and within system constraints imposed by the physical plant. A multi-level i ...

16 Low contention semaphores and ready lists

Peter J. Denning, T. Don Dennis, Jeffrey A. Brumfield

October 1981 **Communications of the ACM**, Volume 24 Issue 10

Full text available:  pdf(1.17 MB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

A method for reducing semaphore and ready-list contention in multiprocessor operating systems is described. Its correctness is established. Its performance is compared with conventional implementations. A ready list implemented as a ring network is proposed and evaluated.

Keywords: memory contention, multiprocessing, process management, ready lists, ring networks, semaphores

17 A multiple processor data flow machine that supports generalized procedures

Arvind, Vinod Kathail

May 1981 **Proceedings of the 8th annual symposium on Computer Architecture**

Full text available:  pdf(725.88 KB)


Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Programs for data flow machines are written in functional languages, some of which require efficient support for dynamic procedure invocation to achieve high performance and programming flexibility. Among the proposed data flow machines, few support procedures in any generality. Our machine, which is a hardware realization of the U-interpreter for data flow languages, provides support for a variety of procedure calling conventions. Because

the U-interpreter assigns a unique activity name to ...

18 Research articles and surveys: Mining data streams: a review


Mohamed Medhat Gaber, Arkady Zaslavsky, Shonali Krishnaswamy
June 2005 **ACM SIGMOD Record**, Volume 34 Issue 2

Full text available:  [pdf\(223.85 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#)

The recent advances in hardware and software have enabled the capture of different measurements of data in a wide range of fields. These measurements are generated continuously and in a very high fluctuating data rates. Examples include sensor networks, web logs, and computer network traffic. The storage, querying and mining of such data sets are highly computationally challenging tasks. Mining data streams is concerned with extracting knowledge structures represented in models and patterns in n ...

19 Local networks

William Stallings
March 1984 **ACM Computing Surveys (CSUR)**, Volume 16 Issue 1


Full text available:  [pdf\(3.01 MB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#), [review](#)

The rapidly evolving field of local network technology has produced a steady stream of local network products in recent years. The IEEE 802 standards that are now taking shape, because of their complexity, do little to narrow the range of alternative technical approaches and at the same time encourage more vendors into the field. The purpose of this paper is to present a systematic, organized overview of the alternative architectures for and design approaches to local networks.

...

20 A unified resource management and execution control mechanism for data flow machines

M. Takesue
June 1987 **Proceedings of the 14th annual international symposium on Computer architecture**

Full text available:  [pdf\(696.18 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

This paper presents a unified resource management and execution control mechanism for data flow machines. The mechanism integrates load control, depth-first execution control, cache memory control and a load balancing mechanism. All of these mechanisms are controlled by such basic information as the number of active state processes, Na. In data flow machines, synchronization among processes is an essential hardware function. Hence, Na can easily be detected by the hardware. Load ...

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